Computer Science and Engineering (CSE)

Courses

CSE 31. Introduction to Digital Systems. 4 Units.
Digital representation of information. Specification, analysis, design and optimization or combinational and sequential logic, register-transfer components and register-transfer systems with datapaths and controllers. Introduction to high-level and algorithmic state-machines and custom processors.

(Design units: 2)
Prerequisite: CSE 41 or I&C SCI 31 or EECS 10 or EECS 12 or ENGRMAE 10 or CSE 21 or I&C SCI 21 or I&C SCI H21
Same as EECS 31.
Restriction: Computer Engineering Majors have first consideration for enrollment. Computer Science Engineering Majors have first consideration for enrollment. Electrical Engineering Majors have first consideration for enrollment.

CSE 31L. Introduction to Digital Logic Laboratory. 3 Units.
Introduction to common digital integrated circuits: gates, memory circuits, MSI components. Operating characteristics, specifications, applications. Design of simple combinational and sequential digital systems (arithmetic processors game-playing machines). Construction and debugging techniques using hardware description languages and CAD tools. Materials fee.

(Design units: 3)
Prerequisite: (EECS 31 or CSE 31) and (EECS 10 or EECS 12 or (CSE 22 or I&C SCI 22) or (CSE 42 or I&C SCI 32))
Same as EECS 31L.
Restriction: Computer Science Engineering Majors have first consideration for enrollment. Electrical Engineering Majors have first consideration for enrollment. Computer Engineering Majors have first consideration for enrollment.

CSE 41. Introduction to Programming. 4 Units.
Introduction to fundamental concepts and techniques for writing software in a high-level programming language. Covers the syntax and semantics of data types, expressions, exceptions, control structures, input/output, methods, classes, and pragmatics of programming.

Same as I&C SCI 31.
Overlaps with I&C SCI 21, CSE 21, I&C SCI H21, EECS 10, EECS 12.
(II and VB ).

CSE 42. Programming with Software Libraries. 4 Units.
Construction of programs for problems and computing environments more varied than in CSE41. Using library modules for applications such as graphics, sound, GUI, database, Web, and network programming. Language features beyond those in CSE41 are introduced as needed.

Prerequisite: I&C SCI 31 or CSE 41. I&C SCI 31 with a grade of C or better. CSE 41 with a grade of C or better
Same as I&C SCI 32.
Overlaps with I&C SCI 22, CSE 22, I&C SCI H22, IN4MATX 42.
(II and (VA or VB )).

CSE 43. Intermediate Programming. 4 Units.
Intermediate-level language features and programming concepts for larger, more complex, higher-quality software. Functional programming, name spaces, modules, class protocols, inheritance, iterators, generators, operator overloading, reflection. Analysis of time and space efficiency.

Prerequisite: I&C SCI 32 or CSE 42. I&C SCI 32 with a grade of C or better. CSE 42 with a grade of C or better
Same as I&C SCI 33.
Overlaps with I&C SCI 22, CSE 22, I&C SCI H22, IN4MATX 42.
(II and VB ).
CSE 45C. Programming in C/C++ as a Second Language. 4 Units.

Prerequisite: I&C SCI 22 or CSE 22 or IN4MATX 42 or I&C SCI 33 or CSE 43 or EECS 40. I&C SCI 22 with a grade of C or better. CSE 22 with a grade of C or better. IN4MATX 42 with a grade of C or better. I&C SCI 33 with a grade of C or better. CSE 43 with a grade of C or better. EECS 40 with a grade of C or better

Same as I&C SCI 45C.

CSE 46. Data Structure Implementation and Analysis. 4 Units.
Focuses on implementation and mathematical analysis of fundamental data structures and algorithms. Covers storage allocation and memory management techniques.

Prerequisite: CSE 45C or I&C SCI 45C. CSE 45C with a grade of C or better. I&C SCI 45C with a grade of C or better

Same as I&C SCI 46.
Overlaps with I&C SCI H23.

(Vb)

CSE 50. Discrete-Time Signals and Systems. 4 Units.
Analysis of discrete-time linear-time-invariant (DTLTI) systems in the time domain and using z-transforms. Introduction to techniques based on Discrete-Time, Discrete, and Fast Fourier Transforms. Examples of their application to digital signal processing and digital communications.

(Design units: 0)

Prerequisite: EECS 70A or CSE 70A

Same as EECS 50.

Restriction: Computer Engineering Majors have first consideration for enrollment. Computer Science Engineering Majors have first consideration for enrollment. Electrical Engineering Majors have first consideration for enrollment.

CSE 70A. Network Analysis I. 4 Units.

(Design units: 1)

Corequisite: MATH 3D

Prerequisite: PHYSICS 7D and (EECS 10 or EECS 12 or ENGRMAE 10 or CSE 41 or I&C SCI 31)

Same as EECS 70A.
Overlaps with ENGRMAE 60.

Restriction: Aerospace Engineering Majors have first consideration for enrollment. Biomedical Engineering Majors have first consideration for enrollment. Civil Engineering Majors have first consideration for enrollment. Computer Engineering Majors have first consideration for enrollment. Electrical Engineering Majors have first consideration for enrollment. Environmental Engineering Majors have first consideration for enrollment. Materials Science Engineering Majors have first consideration for enrollment. Mechanical Engineering Majors have first consideration for enrollment.

CSE 90. Systems Engineering and Technical Communications . 2 Units.
Introduces systems engineering concepts, including specifications and requirements, hardware and software design, integration, testing, and documentation. Emphasizes organization and writing of reports and effective presentations.

Restriction: Computer Science Engineering Majors have first consideration for enrollment.
CSE 112. Electronic Devices and Circuits. 4 Units.
A first course in the design of Very Large Scale Integrated (VLSI) systems. Introduction to CMOS technology; MOS transistors and CMOS circuits. Analysis and synthesis of CMOS gates. Layout design techniques for building blocks and systems. Introduction to CAD tools.

(Design units: 4)
Prerequisite: PHYSICS 7D and (CSE 70A or EECS 70A)
Overlaps with EECS 119, EECS 170D.
Restriction: Computer Science Engineering Majors have first consideration for enrollment.

CSE 132. Organization of Digital Computers. 4 Units.
Building blocks and organization of digital computers, the arithmetic, control, and memory units, and input/output devices and interfaces. Microprogramming and microprocessors.

(Design units: 4)
Prerequisite: EECS 31L or CSE 31L
Same as EECS 112.
Overlaps with COMPSCI 152.
Restriction: Computer Science Engineering Majors have first consideration for enrollment. Computer Engineering Majors have first consideration for enrollment. Electrical Engineering Majors have first consideration for enrollment.

CSE 132L. Organization of Digital Computers Laboratory. 3 Units.
Specification and implementation of a processor-based system using a hardware description language such as VHDL. Hands-on experience with design tools including simulation, synthesis, and evaluation using testbenches.

(Design units: 3)
Prerequisite: EECS 112 or CSE 132
Same as EECS 112L.
Restriction: Computer Science Engineering Majors have first consideration for enrollment. Computer Engineering Majors have first consideration for enrollment.

CSE 135A. Digital Signal Processing. 3 Units.
Nature of sampled data, sampling theorem, difference equations, data holds, z-transform, w-transform, digital filters, Butterworth and Chebychev filters, quantization effects.

(Design units: 2)
Prerequisite: EECS 50 or CSE 50
Same as EECS 152A.
Restriction: Computer Engineering Majors have first consideration for enrollment. Electrical Engineering Majors have first consideration for enrollment. Computer Science Engineering Majors have first consideration for enrollment.

CSE 135B. Digital Signal Processing Design and Laboratory. 3 Units.
Design and implementation of algorithms on a DSP processor and using computer simulation. Applications in signal and image processing, communications, radar, etc.

(Design units: 3)
Prerequisite: (EECS 22 or CSE 45C or I&C SCI 45C) and (EECS 152A or CSE 135A)
Same as EECS 152B.
Restriction: Computer Science Engineering Majors have first consideration for enrollment. Electrical Engineering Majors have first consideration for enrollment. Computer Engineering Majors have first consideration for enrollment.
CSE 141. Concepts in Programming Languages I. 4 Units.
In-depth study of several contemporary programming languages stressing variety in data structures, operations, notation, and control. Examination of different programming paradigms, such as logic programming, functional programming and object-oriented programming; implementation strategies, programming environments, and programming style.

Prerequisite: (I&C SCI 51 or CSE 31 or EECS 31) and (I&C SCI 46 or CSE 46). I&C SCI 51 with a grade of C or better. CSE 31 with a grade of C or better. EECS 31 with a grade of C or better. I&C SCI 46 with a grade of C or better. CSE 46 with a grade of C or better

Same as IN4MATX 101, COMPSCI 141.

Restriction: Computer Science Engineering Majors have first consideration for enrollment. School of Info & Computer Sci students have first consideration for enrollment.

CSE 142. Compilers and Interpreters. 4 Units.
Introduction to the theory of programming language processors covering lexical analysis, syntax analysis, semantic analysis, intermediate representations, code generation, optimization, interpretation, and run-time support.

Prerequisite: CSE 141 or COMPSCI 141 or IN4MATX 101

Same as COMPSCI 142A.

CSE 145. Embedded Software. 4 Units.
Principles of embedded computing systems: embedded systems architecture, hardware/software components, system software and interfacing, real-time operating systems, hardware/software co-development, and communication issues. Examples of embedded computing in real-world application domains. Simple programming using an embedded systems development environment.

Corequisite: COMPSCI 145L
Prerequisite: (CSE 46 or I&C SCI 46) and (I&C SCI 51 or CSE 132 or EECS 112)

Same as COMPSCI 145.

Restriction: Computer Science Engineering Majors have first consideration for enrollment. Computer Science Majors have first consideration for enrollment.

CSE 145L. Embedded Software Laboratory. 2 Units.
Laboratory section to accompany CSE 145 or COMPSCI 145.

(Design units: 0)

Corequisite: CSE 145 or COMPSCI 145.

Same as COMPSCI 145L.

CSE 161. Design and Analysis of Algorithms. 4 Units.
Techniques for efficient algorithm design, including divide-and-conquer and dynamic programming, and time/space analysis. Fast algorithms for problems applicable to networks, computer games, and scientific computing, such as sorting, shortest paths, minimum spanning trees, network flow, and pattern matching.

Prerequisite: (I&C SCI 46 or CSE 46) and I&C SCI 6B and I&C SCI 6D and MATH 2B. I&C SCI 46 with a grade of C or better. CSE 46 with a grade of C or better

Same as COMPSCI 161.

Restriction: School of Info & Computer Sci students have first consideration for enrollment. Computer Science Engineering Majors have first consideration for enrollment.
CSE 181A. Senior Design Project I. 3 Units.
Teaches problem definition, detailed design, integration, and testability with teams of students specifying, designing, building, and testing complex systems. Lectures include engineering values, discussions, and ethical ramifications of engineering decisions. Materials fee.

(Design units: 3)
Prerequisite: EECS 113 or EECS 170C or CSE 145A or COMPSCI 145A
Same as EECS 159A.

Restriction: Computer Science Engineering Majors have first consideration for enrollment. Electrical Engineering Majors have first consideration for enrollment. Computer Engineering Majors have first consideration for enrollment.

CSE 181B. Senior Design Project II. 3 Units.
Teaches problem definition, detailed design, integration, and testability with teams of students specifying, designing, building, and testing complex systems. Lectures include engineering values, discussions, and ethical ramifications of engineering decisions. Materials fee.

(Design units: 3)
Prerequisite: EECS 159A or CSE 181A
Same as EECS 159B.

Restriction: Computer Science Engineering Majors have first consideration for enrollment. Electrical Engineering Majors have first consideration for enrollment. Computer Engineering Majors have first consideration for enrollment.

CSE H198. Honors Research in CSE. 4 Units.
Directed independent research in computer science and engineering for honors students.

Repeatability: May be repeated for credit unlimited times.
Restriction: Campuswide Honors Program students only. Bren School of ICS Honors students only. Upper-division students only. Computer Science Engineering Majors only.

CSE 199. Individual Study. 1-4 Units.
Supervised independent reading, research, or design for undergraduate Engineering majors. Students taking individual study for design credit are to submit a written paper to the instructor and to the Undergraduate Student Affairs Office in the School of Engineering.

(Design units: 1-4)
Repeatability: May be taken for credit for 8 units.